

# PDTC124EU-Q

50 V, 100 mA NPN resistor-equipped transistor; R1 = 22 k $\Omega$ , R2 = 22 k $\Omega$ 

7 March 2022

Product data sheet

### 1. General description

NPN Resistor-Equipped Transistor (RET) in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTA124EU

#### 2. Features and benefits

- 100 mA output current capability
- · Built-in bias resistors
- · Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

- Digital application in automotive and industrial segments
- · Cost-saving alternative for BC847-Q series in digital applications
- · Controlling IC inputs
- Switching loads

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	15.4	22	28.6	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	

[1] See "Section 11: Test information" for resistor calculation and test conditions.



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	1	input (base)	3	
2	GND	ground (emitter)		R <sub>1</sub>
3	0	output (collector)	3C-70 (SOT323)	GND R2

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PDTC124EU-Q		plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PDTC124EU-Q	806

[1] % = placeholder for manufacturing site code

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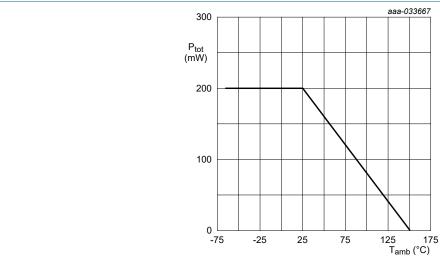
# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	50	V
$V_{EBO}$	emitter-base voltage	open collector		-	10	V
VI	input voltage	positive		-	40	V
		negative		-	-10	V
Io	output current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.



FR4 PCB, single-sided, 35  $\mu m$  copper, tin-plated and standard footprint

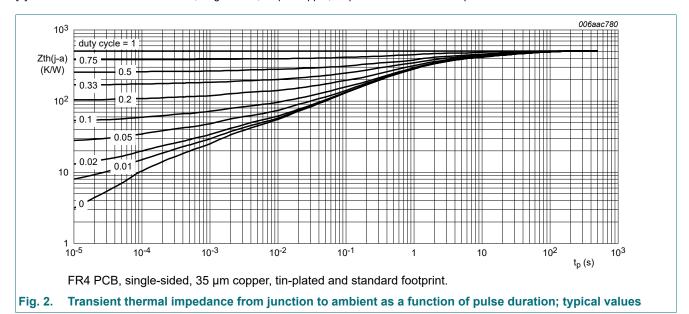
Fig. 1. Power derating curve

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

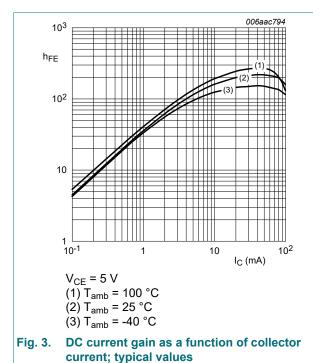


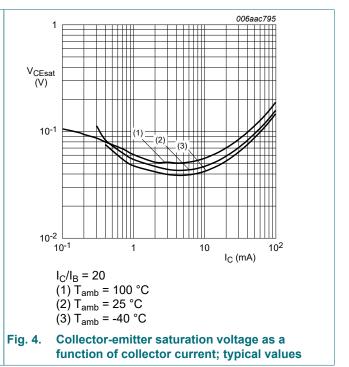
### 10. Characteristics

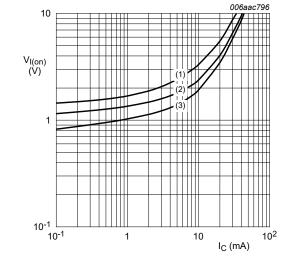
**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	= 2 mA; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		50	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	CB = 50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
I <sub>CEO</sub>	collector-emitter cut-off	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	180	μΑ
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C		60	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	150	mV
V <sub>I(off)</sub>	off-state input voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 μA; T <sub>amb</sub> = 25 °C		-	1.1	0.8	V
V <sub>I(on)</sub>	on-state input voltage	V <sub>CE</sub> = 0.3 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C		2.5	1.7	-	V
R1	bias resistor 1 (input)		[1]	15.4	22	28.6	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	
C <sub>c</sub>	collector capacitance	$V_{CB}$ = 10 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C		-	-	2.5	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = 5 V; $I_{C}$ = 10 mA; f = 100 MHz; $T_{amb}$ = 25 °C	[2]	-	230	-	MHz

- [1] See "Section 11: Test information" for resistor calculation and test conditions.
- [2] Characteristics of built-in transistor.

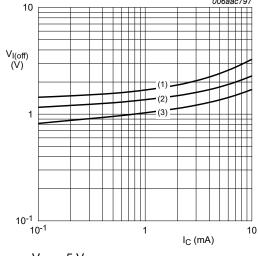






 $V_{CE} = 0.3 V$ 

(1) T<sub>amb</sub> = -40 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = 100 °C

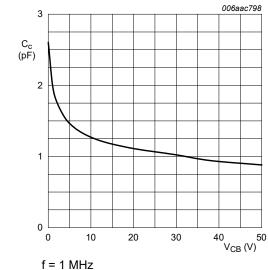


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V<sub>CE</sub> = 5 V (1) T<sub>amb</sub> = -40 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = 100 °C

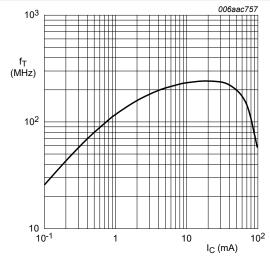
Fig. 5. On-state input voltage as a function of collector | Fig. 6. current; typical values





 $T_{amb}$  = 25 °C

Fig. 7. Collector capacitance as a function of collectorbase voltage; typical values



f = 100 MHz  $T_{amb} = 25 \, ^{\circ}C$ 

 $V_{CE} = 5 V$ 

Transition frequency as a function of collector Fig. 8. current; typical values of built-in transistor

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50 V, 100 mA NPN resistor-equipped transistor; R1 = 22 k $\Omega$ , R2 = 22 k $\Omega$ 

### 11. Test information

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

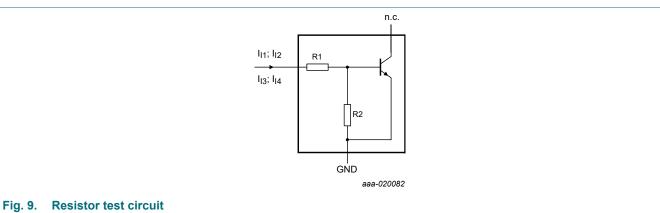
#### **Resistor calculation**

Calculation of bias resistor 1 (R1)

$$R1 = \frac{V(I12) - V(I11)}{I12 - I11}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I14) - V(I13)}{R1 \cdot (I14 - I13)} - 1$$

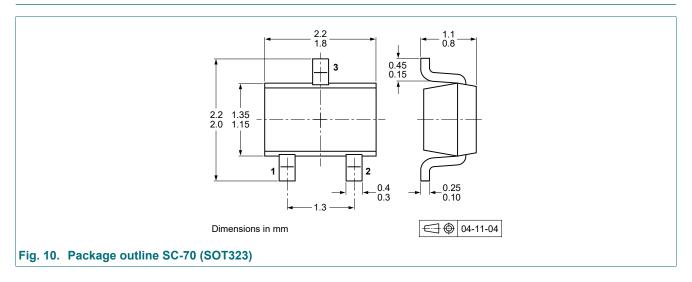


#### **Resistor test conditions**

**Table 8. Resistor test conditions** 

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I <sub>I1</sub>	I <sub>I2</sub>	I <sub>13</sub>	I <sub>14</sub>
PDTC124EU-Q	22	22	150 µA	230 μΑ	-150 μΑ	-230 µA

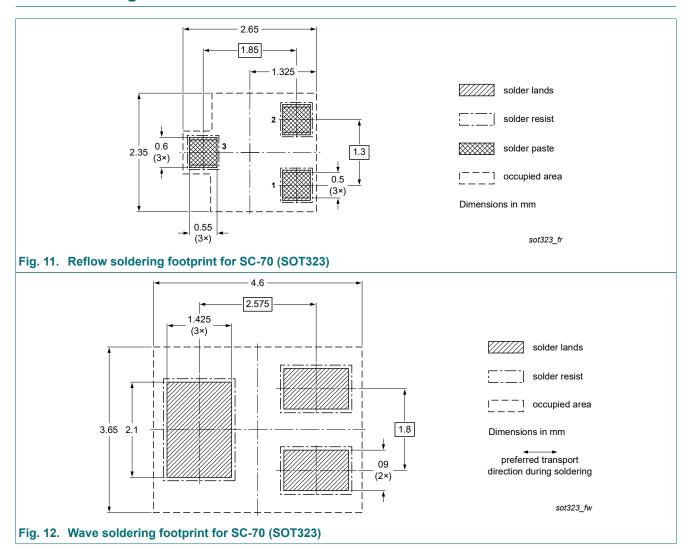
# 12. Package outline



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50 V, 100 mA NPN resistor-equipped transistor; R1 = 22 k $\Omega$ , R2 = 22 k $\Omega$ 

# 13. Soldering



# 14. Revision history

### Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTC124EU-Q v.1	20220307	Product data sheet	-	-

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Date of release: 7 March 2022

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