NOT RECOMMENDED FOR NEW DESIGNS



Micro Commercial Components



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Features

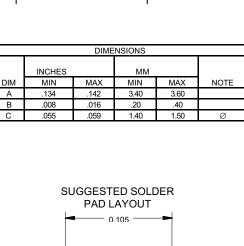
- The three layer, two terminal, axial lead, hermetically sealed diacs are designed specifically for triggering thyristors.
- Lead Free Finish/Rohs Compliant (Note1) ("P"Suffix designates Compliant. See ordering information)
- Moisture Sensitivity: Level 1 per J-STD-020C
- These diacs are intended for use in thyrisitors phase control, circuits for lamp dimming, universal motor speed control, and heat control.

Maximum Ratings

- Operating Temperature: -40°C to +110°C
- Storage Temperature: -40°C to +125°C

Electrical Characteristics @ 25°C Unless Otherwise Specified

Power dissipation on Printed Circuit(I=10mm)	Pc	150mW	T _A =50°C
Repetitive Peak on-state Current	I _{TRM}	2.0A	t _p =10us,f=100Hz
Breakover Voltage	V _{BO}	Min Typ Max 35 40 45V	C=22nF(Note 3)
Breakover Voltage Symmetry	+V _{BO} - -V _{BO}	±3V	C=22nF(Note 3)
Output Voltage(Note 2)	$V_{o(min)}$	5V	
Breakover Current(Note 2)	I _{BO(max)}	100uA	C=22nF
Rise Time(Note 2)	T _r	1.5us	
Leakage Current(Note 2)	I _{B(max)}	10uA	$V_B=0.5V_{BO(max)}$



В

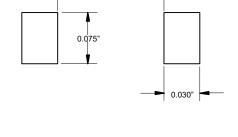
LLDB4

SILICON

BIDIRECTIONAL

DIAC

MINIMELF



Note: 1. Lead in Glass Exemption Applied, see EU Directive Annex 7(C)-I.

2. Electrical characteristics applicable in both forward and

reverse directions.
Connected in parallel with the devices.

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Revision: B



LLDB4

DIAGRAM 1: Current-voltage characteristics

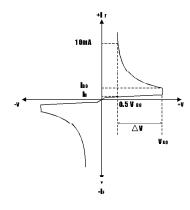


FIG.1-Power dissipation versus ambient temperature (maximum values)

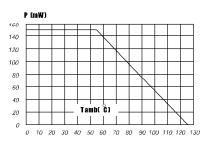
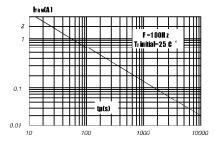


FIG.3-Peak pulse current versus pulse duration (maximum values)



DLAGR AM 2: T est circuit for output voltage

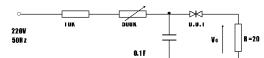


DIAGRAM 3: T est aircuit see alagram2 adjust R for I=0:5A

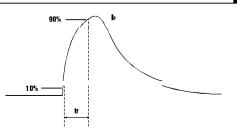
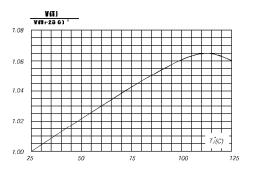


FIG.2-Relative variation of VBO versus junction temperature(typical values)



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Ordering Information :

Device	Packing	
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel	

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