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5-to-1.8V Converter Works Without Magnetics

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Abstract: To derive 1.8V from 5V, you might think of using a switch-mode regulator. Switchers are highly efficient but require inductors. Linear regulators are simple, but would have only 40% efficiency. This circuit is more than 70% efficient (Figure 2a), sources as much as 100 mA, and uses no inductor.

To derive 1.8V from 5V, you might think of using a switch-mode regulator. Switchers are highly efficient but also complicated and expensive. Linear regulators, too, are out of the question unless your design can tolerate 40% efficiencies. The circuit in **Figure 1**, on the other hand, is more than 70% efficient (**Figure 2a**), sources as much as 100 mA, costs less than a switch-mode regulator, and requires less space. IC₁ is a CMOS charge-pump voltage converter that the circuit configures as a voltage inverter. With its output grounded and 5V at its V+ pin, IC₁ generates V+/2, or approximately 2.5V at Pin 3. This nominal 2.5V output, which sags as the device sources current, drives linear-regulator IC₂, which regulates the 2.5V input to 1.8V. IC₂ is capable of sourcing 100 mA before its sagging input voltage falls below the dropout level (**Figure 2b**). Using larger values for C₁ and C₂ enables IC₁ to maintain its output voltage with heavier load currents. If the load current drawn from this circuit is 50mA or less, you can substitute the MAX1730 for this circuit. This regulated charge pump is designed specifically to step voltages down to a lower level. (DI #2511)

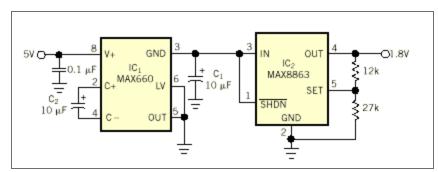


Figure 1. A 5-to-1.8V converter uses a charge-pump IC to lower the input voltage and then uses a linear regulator to achieve the desired output level.

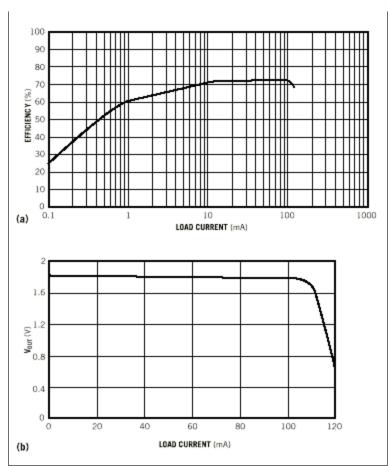


Figure 2. The circuit provides maximum efficiency for load currents of 10 to 100 mA (a) and maintains its output level for load currents as high as 100 mA (b).

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